

Amendments to the Abstract

Please amend the Abstract Of The Disclosure as follows.

Abstract Of The Disclosure

A multiplexer circuit (100) comprises at least two input channels (IN_0 , IN_1) and an output channel (2), each input channel (IN_0 , IN_1) comprising a first transmission gate (FT_0 , FT_1) which can be switched by a select signal ($SELECT_0$, $\overline{SELECT_0}$; $SELECT_1$, $\overline{SELECT_1}$) for connecting the input channel (IN_0 , IN_1) to the output channel (2), and wherein at least one of the input channels (IN_0 , IN_1) comprises a bypass circuit for preventing a current flowing through the first transmission gate (FT_0 , FT_1) from reaching the other input channel, and a second transmission gate (ST_0 , ST_1).

(Fig. 2)